

EIC 2800

SEARCH REPORT



STIC Database Tracking Number: 357354

To: SEUNGSOOK HAM
Location: JEF-8D65
Art Unit: 2817
Monday, February 28, 2011

Case Serial Number: 10/644757
Patent: 6276980

From: DIANE JACKSON
Location: EIC2800
JEF-4B68
Phone: (571)272-3260

diane.jackson@uspto.gov

Search Notes

Hi,

Attached are litigation search results in Lexis Nexis, and CourtLink and Q-Pat/Orbit.

No Litigation was found for Serial Number 10/644757 and Patent Number 6276980.

If you have any questions, please feel free to contact me.

Thanks,

Diane

Jackson, Diane

357354

From: Ham, Seungsook
Sent: Monday, February 28, 2011 9:46 AM
To: STIC-EIC2800
Subject: litigation search for reissue application: 10/644757(USP 6276980)

Could you do a litigation search for above reissue application and send me a copy?

Thanks,
Seungsook(Robyn) Ham
U.S. Patent & Trademark Office
TC 2800 TQAS (detailee)
Jeff. bldg. 8D65
571-272-2405

6276, 980

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Method for forming electrode for plasma display panel

AB
(US6276980)

The present invention relates to a discharge sustaining electrode formed of a transparent electrode and a non-transparent electrode for a plasma display panel (PDP), and it is an object of the present invention to provide a method for an electrode which is well applicable to forming a non-transparent electrode using an Ag material and providing a good productivity and a certain contrast characteristic. The method for forming a bus electrode according to the present invention includes a first step for coating Ag paste including some black powder having different specific gravity particles and some Ag white powder on the transparent electrode, a second step for leveling-separating the black and white powders contained in the coated Ag paste based on a specific gravity difference for a certain time, and a third step for burning out a binder from the coated Ag paste to thereby implementing a firing process. Therefore, it is possible to implement a two-tier bus electrode structure based on one time paste printing operation to thereby decrease a formation process of an electrode.

IN KIM SANG-TAE
OIN サンータエ・キム
PA LG ELECTRONICS
PA0 LG Electronics Inc., Seoul (KR)
OPA エルジー電子株式会社

Published As

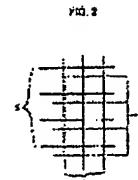
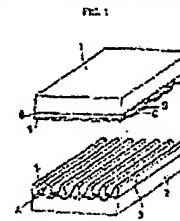
	Publ. number	Publ. date	Appl. number	Appl. date	Publ.	Link
JP	JP2000057943	20000225	1999JP-0222231	19990805	A - ...	
KR	KR20000013193	20000306	1998KR-0031912	19980805	A - ...	
JP	JP3108875	20001113			B2 - ...	
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365879 (09) 6276980 August 21, 2001

UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT

6276980

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August 21, 2001

Method for forming electrode for plasma display panel

REISSUE: August 21, 2003 - Reissue Application filed Ex. Gp.: 2879; Re. S.N. 10/644,757 (O.G. November 11, 2003)

August 29, 2006 - Reissue Application filed, Ex. Gp.: 2879; Re. S.N. 11/511,535 (O.G. October 10, 2006)

August 29, 2006 - Reissue Application filed, Ex. Gp.: 2879; Re. S.N. 11/511,536 (O.G. October 10, 2006)

August 29, 2006 - Reissue Application filed, Ex. Gp.: 2879; Re. S.N. 11/511,537 (O.G. October 10, 2006)

August 29, 2006 - Reissue Application filed, Ex. Gp.: 2879; Re. S.N. 11/511,538 (O.G. October 10, 2006)

INVENTOR: Kim, Sang-Tae - Seoul, Korea, South (KR)

APPL-NO: 365879 (09)

FILED-DATE: August 3, 1999

GRANTED-DATE: August 21, 2001

PRIORITY: August 5, 1998 - 9831912, Korea, South (KR)

ASSIGNEE-PRE-ISSUE: August 3, 1999 - ASSIGNMENT OF ASSIGNS INTEREST (SEE DOCUMENT FOR DETAILS)., LG ELECTRONICS INC. 20, YOIDO-DONG, YOUNGDUNGPO-GUSEOUL, (1), Reel and Frame Number: 010153/0368

ASSIGNEE-AT-ISSUE: LG Electronics Inc., Seoul, Korea, South (KR), Foreign company or corporation (03)

LEGAL-REP: Fleshner & Kim, LLP

PUB-TYPE: August 21, 2001 - Utility Patent having no previously published pre-grant publication (B1)

PUB-COUNTRY: United States (US)

US-MAIN-CL: 445#24

US-ADDL-CL: 427#125

Application Number Information**Application Number Information**Application Number: 10/644757 [Assignments](#)Filing or 371(c) Date: 08/21/2003 [eDan](#)

Effective Date: 08/21/2003

Application Received: 08/21/2003

Patent Number:

Issue Date: 00/00/0000

Date of Abandonment: 00/00/0000

Attorney Docket Number: RPL-0010 REI

Status: 77 /RESPONSE TO EX PARTE QUAYLE ACTION ENTERED AND FORWARDED TO EXAMINER

Confirmation Number: 7559

Title of Invention: METHOD FOR FORMING ELECTRODE FOR PLASMA DISPLAY PANEL

Examiner Number: 73924 / WILLIAMS, JOSEPH[IRW Madras](#)

Group Art Unit: 2889

Class/Subclass: 445/024.000

Lost Case: NO

Interference Number:

Unmatched Petition: NO

L&R Code: Secrecy Code:1

Third Level Review: NO

Secrecy Order: NO

Status Date: 08/22/2010

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Continuity/Reexam Information for 10/644757

Parent Data

10644757, filed 08/21/2003 is a reissue of 09365879, filed 08/03/1999, now U.S. Patent #6276980
claims foreign priority to P98-31912, filed 08/05/1998

Child Data

11511535, filed on 08/29/2006, now abandoned is a division of 10644757, filed on 08/21/2003
11511536, filed on 08/29/2006, now abandoned is a division of 10644757, filed on 08/21/2003
11511537, filed on 08/29/2006, now abandoned is a division of 10644757, filed on 08/21/2003
11511538, filed on 08/29/2006, now abandoned is a division of 10644757, filed on 08/21/2003

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Effective Date: 08/03/1999

Application Received: 08/03/1999

Patent Number: 6276980

Issue Date: 08/21/2001

Date of Abandonment: 00/00/0000

Attorney Docket Number: RPL-010

Status: 150 /PATENTED CASE

Confirmation Number: 2394

Title of Invention: METHOD FOR FORMING ELECTRODE FOR PLASMA DISPLAY PANEL

Examiner Number: 61236 / **RAMSEY, KENNETH**Group Art Unit: **2879**

Class/Subclass: 445/024.000

Lost Case: NO

Interference Number:

Unmatched Petition: NO

L&R Code: Secrecy Code:**1**

Third Level Review: NO

Secrecy Order: NO

Status Date: 08/06/2001

Oral Hearing: NO

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09365879	9200	04/29/2009	No Charge to Location	No Charge to Name	SIDDIQI,VIOAR	

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